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CMOS VLSI Design and Circuit Simulation Tasks

David Money Harris Associate Professor of Engineering at Harvey Mudd College in Claremont, CA, holds a Ph.D. from Stanford University and S.B. and M.Eng. degrees from MIT. His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has ...

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Funded by both Qualcomm and NSF, the USC Asynchronous CAD/VLSI group now investigates next generation asynchronous circuits and CAD flows, including research topics in design, analysis, synthesis, test, and verification. It is located within the Ming Hsieh Department of Electrical Engineering of the University of Southern California.

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